



ATTY-DOCKET NO. BU998050DIV2	SERIAL NO. 09/805,137
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FILING DATE March 14, 2001	GROUP 2825

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,867,644	02/02/99	Ranson, et al.			
	6,237,101 B1	05/22/01	Moore, et al.			
	5,862,148	01/19/99	Typaldos, et al.			
	2002/0013912 A1	01/31/02	Lin			
	5,732,255	03/24/98	Verbauwhede			
	5,276,824	01/04/94	Skruhak, et al.			
	5,790,874	08/04/98	Takano, et al.			

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		J. Patrick Brennan, et al. "Low Power Methodology and Design Techniques for Processor Design", 1998, Pages 268-273.

EXAMINER 	DATE CONSIDERED 3-May-2003
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.